**DAILY ASSESSMENT FORMAT**

|  |  |  |  |
| --- | --- | --- | --- |
| **Date:** | **4th August 2020** | **Name:** | **Pragati M Kundalkar** |
| **Course:** | **Coursera** | **USN:** | **4AL17EC072** |
| **Topic:** | **VLSI CAD part 1: logic** | **Semester & Section:** | **6 & B** |
| **GitHub Repository:** | **Pragati-m-k** |  |  |

|  |
| --- |
| **SESSION DETAILS**  **Session images** |